Cp340 rs232c manual

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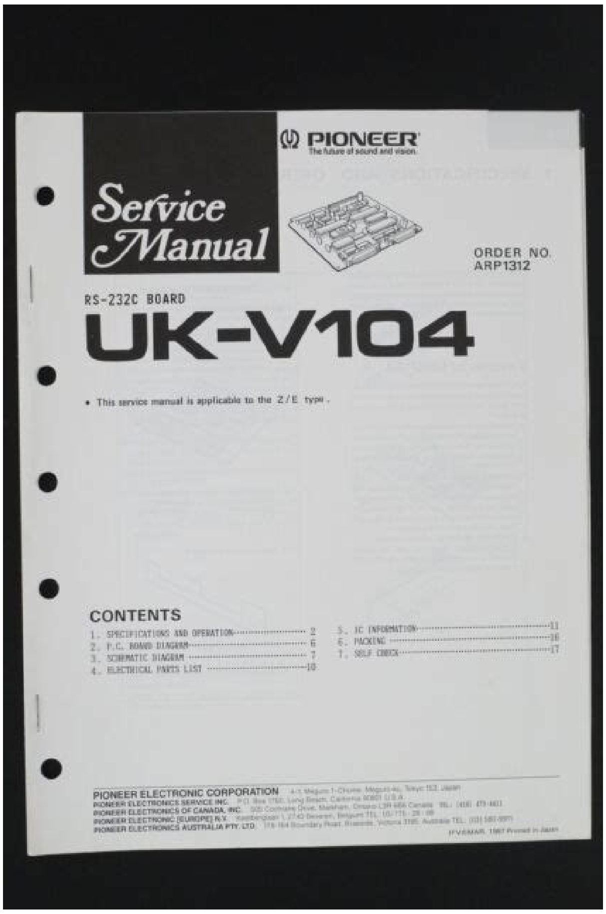

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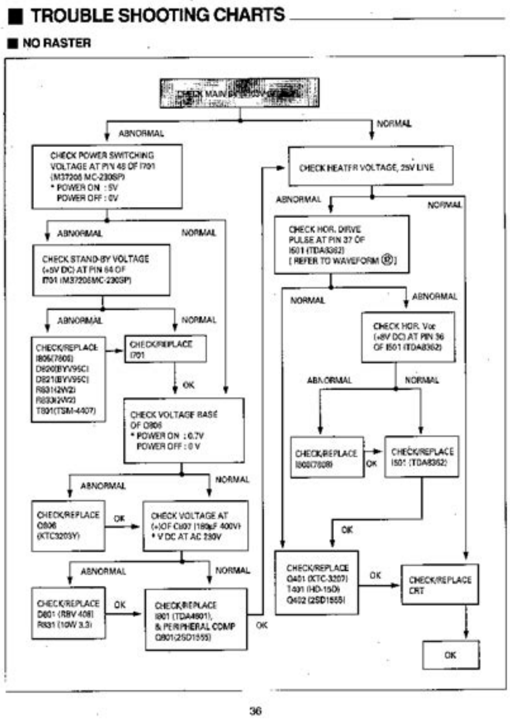

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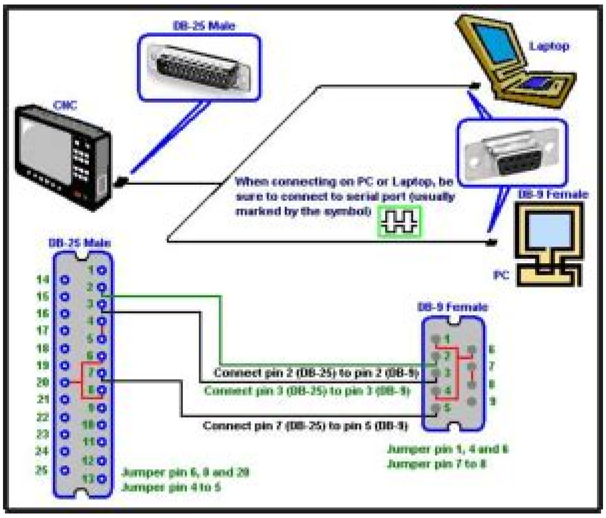

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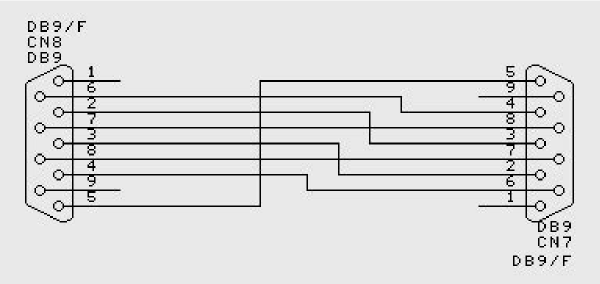

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 Disclaimer of Liability We have reviewed the contents of this publication to ensure consistency with the hardware and software described. Since variance cannot be precluded entirely, we cannot guarantee full consistency. However, the information in this publication is reviewed regularly and any necessary corrections are included in subsequent editions. Siemens AG Automation and Drives Postfach 4848, Nuremberg, Germany Siemens AG 2005 Technical data subject to change Siemens Aktiengesellschaft A5E Contents of the manual This manual describes the hardware and software of communication processor CP 340, and the implementation into the S7300 automation system. We reserve the right to describe changes to module functionality in a product information sheet.Following the appendices, a glossary defines important technical terms used in the manual. At the end of the manual a comprehensive index allows quick access to information on specific subjects. Additional Assistance Please contact your local Siemens representative if you have any queries about the products described in this manual. Training Center We offer a range of courses to help get you started with the S7 programmable controller. Please contact your local training center or the central training center in Nuremberg, D Germany. A forum, where users and experts from all over the world exchange their experiences. Information about onsite services, repairs and spare parts.Functionality of the CP 340 The CP 340 communications processor provides the following functionality Transmission rate up to 19.

2 Kbaud, half duplex Integration of the most important transmission protocols in the module firmware 3964R procedure ASCII driver Printer driver Adaptation of the transmission protocols by using the CP 340 Configuring PtP communication integrated serial interface Three module variants are available, each having a different interface type that is suitable for different communication partners see Module Variants Table. Uses of the CP 340 The communications processor CP 340 supports PtP communication with diverse Siemens modules and products of other manufacturers SIMATIC S5 using the 3964R driver with corresponding interface module on the S5 side. Barcode reader, using the 3964R or ASCII drivers PLCs from other manufacturers using the 3964R or ASCII

driver.Table 13 HardwareComponents for a PointtoPoint Connection Components Function Diagram Mounting rack.Backup battery Communications processor.Programming device PG or PC.Table 14 Software Components for a PointtoPoint Connection with the CP 340 Components Function Diagram STEP 7 software package.Position of Module Elements The figure shows the positions of the module elements on the front panel of the CP 340 communications processor. Connector for the S7 backplane bus A bus connector is supplied with the CP 340. The bus connector is plugged onto the back panel of the CP 340 when mounting the CP 340. The S7300 backplane bus is connected via the bus connector. The S7300 backplane bus is a serial data bus with which the CP 340 communicates with the modules of the programmable controller and is supplied with the necessary voltage.For pointtopoint connections between the CP 340 and a communication partner, Siemens offers standard connecting cables in various lengths RS 232C interface of the CP 340RS 232C Properties The RS 232C interface is a voltage interface used for serial data transmission in compliance with the RS 232C standard.

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Type Voltage Interface Front connector 9pin subd male with screw interlock RS 232C Signals TXD, RXD, RTS, CTS, DTR, DSR, RI, DCD, GND; all isolated against S7internal power supply Max.The simplest form of data interchange is via a pointtopoint connection between two communication partners. Pointtopoint communication In pointtopoint communication the communications processor forms the interface between a programmable controller and a communication partner. In PtP communication with communication processor, data are transferred via serial interface. Serial Transmission In serial transmission, the individual bits of each byte of information are transmitted one after the other in a fixed order. The CP 340 is equipped with three different drivers for this purpose. Unidirectional data traffic Printer Driver Bidirectional data traffic ASCII driver 3964R procedure The CP 340 handles data transmission via the serial interface in accordance with the interface type and the selected driver. Unidirectional Data Traffic Printer Output In the case of printer output printer driver, n bytes of user data are output to a printer. No characters are received. In halfduplex operation, therefore, at any one time data is being either sent or received. Fullduplex operation ASCII driver Data are exchanged between two or more communication partners in both directions simultaneously. In fullduplex mode, data can be sent and received at the same time. Every communication partner must be able to operate a send and a receive facility simultaneously. Asynchronous Data Transmission With the communications processor, serial transmission occurs asynchronously. The socalled timebase synchronism a fixed timing code used in the transmission of a fixed character string is only upheld during transmission of a character. Each character to be sent is preceded by a synchronization impulse, or start bit. The length of the startbit transmission determines the clock pulse.

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The end of the character transmission is signaled by the stop bit. Declarations As well as the start and stop bits, further declarations must be made between the sending and receiving partners before serial transmission can take place. These include Transmission speed baud rate Character and acknowledgment delay times Parity Number of data bits Number of stop bits Number of setup and transmission attempts permitted Character frame Data is transmitted between the CP 340 and a communication partner via the serial interface in a 10bit or 11bit character frame. Three data formats are available for each character frame. This is known as the character delay time. The ISO has defined a 7layer model, which is recognized as the basis for a worldwide standardization of transmission protocols for computertocomputer communication. ISO 7Layer Reference Model for Data Transmission All communication partners must adhere to a fixed set of rules for handling and implementing data traffic. Such rules are called protocols. Protocol A protocol defines the following points Operating mode Halfduplex or fullduplex operation Initiative Which communication partners can initiate the transmission and under what conditions Control characters Which control characters are to be used for data transmission Character frame Which character frames are to be used for data transmission. Data backup The data backup procedure to be used Character delay time The time period within which an incoming character must be received. Each protocol layer, except for the lowest one, is embedded in the next one down. If the sending and receiving partners both use the same protocol, layer 6 can be omitted. Generally speaking, the more layers of the reference model are applied, the greater the transmission integrity.

Classifying the Supplied Protocols The CP 340 governs the following protocols 3964R procedure ASCII driver Printer Driver The figure below illustrates how these supplied protocols of the CP 340 fit into the ISO reference model Figure 24 Position of the Supplied Protocols of the CP 340 in the ISO Reference Model Transmission Integrity with the Printer Driver Data Integrity When Using the Printer Driver No data integrity precautions are taken for data transmission with the printer driver. When data is output to the printer, the printers BUSY signal is evaluated. The CP 340 receives the BUSY signal as a CTS signal and evaluates it in the same way see ASCII driver. This means that, although this type of data transport has a very efficient throughput rate, security is not guaranteed. Using the parity bit ensures that the inversion of a bit in a character to be transmitted can be recognized. If two or more bits of a character are inverted, this error can no longer be detected. To increase transmission integrity, a checksum and length specification for a message frame can be employed. These measures must be implemented by the user. A further increase in data integrity can be achieved by means of acknowledgment message frames in response to send or receive message frames. This is also the case with highlevel protocols for data communication see ISO 7layer reference model. Transmission Integrity with 3964R Enhanced Data Integrity with the 3964R Procedure The Hamming distance with the 3964R is 3. This measures the integrity of data transmission. The 3964R procedure ensures high transmission integrity on the data line. This high integrity is achieved by means of a fixed messageframe setup and cleardown as well as the use of a block check character BCC.

Two different procedures for data transmission can be used, either with or without a block check character data transmission without a block check character 3964 data transmission with a block check character 3964R In this manual, the designation 3964R is used when descriptions and notes refer to both data transmission procedures. You can only ensure this by using a programmable acknowledgment mechanism. The block check of the 3964R procedure EXOR operation cannot detect missing zeros as a whole character because a zero in the EXOR operation does not affect the result of the calculation. Although the loss of an entire character this character has to be a zero! is highly unlikely, it could possibly occur under very bad transmission conditions. You can protect a transmission against such errors by sending the length of the data message along with the data itself, and having the length checked at the other end. As well as the physical layer layer 1, the 3964R procedure also incorporates the datalink layer layer 2 Control characters Introduction During data transmission, the 3964R procedure adds control characters to the user data datalink layer. These control characters allow the communication partner to check whether the data has arrived complete and without errors. The control characters of the 3964R Procedure The 3964R procedure analyzes the following control codes STX Start of Text; Start of the string to be transmitted DLE Data Link Escape; Data Link Escape ETX End of Text; End of string to be transmitted BCC Block Check Character only with 3964R; Block Check Character NAK Negative Acknowledgement; Negative Acknowledgement Note If DLE is transmitted as an information string, it is sent twice so that it can be distinguished from the control code DLE during connection setup and release on the send line DLE duplication. The receiver then reverses the DLE duplication.

Priority With the 3964R procedure, one communication partner must be assigned a higher priority and the other partner a lower priority. Figure 25 Block Checksum The block checksum is the even longitudinal parity EXOR operation on all data bytes of a sent or received block. Its calculation begins with the first byte of user data first byte of the message frame after the connection setup, and ends after the DLE ETX code on connection release. Note If DLE duplication occurs, the DLE code is accounted for twice in the BCC calculation. Figure 26 Data Traffic when Sending with the 3964R Procedure Establishing a Send Connection To establish the connection, the 3964R procedure sends the control code STX. If the communication partner responds with the DLE code before the acknowledgment delay time expires, the procedure switches to send mode. If the communication partner answers with NAK or with any other control code except for DLE, or the acknowledgment delay time expires without a response, the procedure repeats the connection setup. After the defined number of unsuccessful setup attempts, the procedure aborts the connection setup and sends the NAK code to the communication partner. Sending Data If a connection is successfully established, the user data contained in the output buffer of the CP 340 is sent to the communication partner with the chosen transmission parameters. The partner monitors the times between incoming characters. If a different code is sent, the procedure first waits for the character delay time to expire and then sends the NAK code to change the mode of the communication partner to idle. Then the procedure starts to send the data again with the connection setup STX. Once the contents of the buffer have been sent, the procedure adds the codes DLE, ETX and with the 3964R only the block checksum BCC as the end identifier, and waits for an acknowledgment code.

If the communication partner sends the DLE code within the acknowledgment delay time, the data block has been received without errors. If the communication partner responds with NAK, any other code except DLE, or a damaged code, or if the acknowledgment delay time expires without a response, the procedure starts to send the data again with the connection setup STX. After the defined number of attempts to send the data block, the procedure stops trying and sends an NAK to the communication partner. If the idle procedure receives any control code except for STX or NAK, it waits for the character delay time to expire, then sends the code NAK.Incoming receive characters are now stored in the receive buffer. If two consecutive DLE codes are received, only one of these is stored in the receive buffer. After each receive character, the procedure waits out the character delay time for the next character. If this period expires before another character is received, an NAK is sent to the communication partner. If no empty receive buffer is available during a connection setup with STX, a wait time of 400 ms is started. If there is still no empty receive buffer after this time has expired, the system program reports the error error message in STATUS output of FB, and the procedure sends a NAK and returns to idle mode. Otherwise, the procedure sends a DLE and receives the data as described above. Releasing a Receive Connection If transmission errors occur during receiving lost character, frame error, parity error, etc., the procedure continues to receive until the connection is shut down, then an NAK is sent to the communication partner. A repetition is then expected. If the undamaged block still cannot be received after the number of repeat attempts defined on parameter assignment, or if the communication partner does not start the repetition within a block wait time of 4 seconds, the procedure aborts the receive operation.

When the 3964 procedure detects a DLE ETX character string, it ends the receiving operation and confirms the successfully received block by sending a DLE signal to the communication partner. When errors are found in the received data, it outputs a NAK signal to the communication partner. A repetition is then expected. If the CP 340 recognizes the string DLE ETX BCC, it stops receiving and compares the received block check character with the longitudinal parity calculated internally. If the BCC is correct and no other receive errors have occurred, the CP 340 sends the code DLE to the communication partner. If the BCC is correct and no other receive errors have occurred, the 3964R procedure sends a DLE and returns to idle mode. If the BCC is faulty or a different receiving error occurs, an NAK is sent to the communication partner. A repetition is then expected. Figure 28 Data Traffic when Receiving Errored Data When DLE, ETX, BCC is received, the CP 340 compares the

BCC of the communication partner with its own internally calculated value. If the BCC is correct and no other receive errors occur, the CP 340 responds with DLE. Otherwise, it responds with an NAK and waits the block wait time T of 4 seconds for a new attempt. If after the defined number of transmission attempts the block cannot be received, or if no further attempt is made within the block wait time, the CP 340 aborts the receive operation. Figure 29 Data Traffic during an Initialization Conflict If a device responds to the communication partners send request code STX within the acknowledgment delay time by sending the code STX instead of the acknowledgment DLE or NAK, an initialization conflict occurs. Both devices want to execute a send request. The device with the lower priority withdraws its send request and responds with the code DLE. The device with the higher priority sends its data in the manner described above.